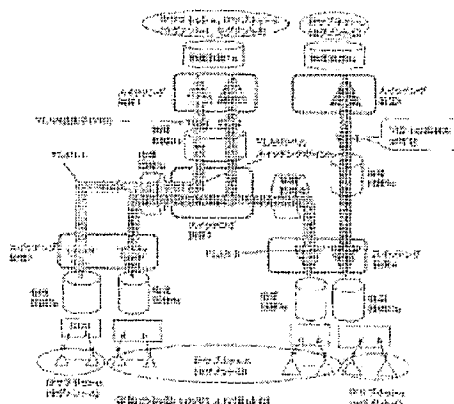


VLAN AND VLAN FRAME SWITCHING APPARATUS

D3

Publication number: JP2003244185 (A)**Publication date:** 2003-08-29**Inventor(s):** ISHIDA HIROSHI**Applicant(s):** MATSUSHITA ELECTRIC IND CO LTD**Classification:**- **international:** H04L12/56; H04L12/46; H04L12/56; H04L12/46; (IPC1-7): H04L12/46; H04L12/56- **European:****Application number:** JP20020040108 20020218**Priority number(s):** JP20020040108 20020218**Abstract of JP 2003244185 (A)**

PROBLEM TO BE SOLVED: To simplify an address retrieving process and apply it to a large-scale network. ; **SOLUTION:** Upon input of frames, a header checker 111 in an input processor 11 decides whether a VLAN tag is attached to the input frame, and, if not attached, retrieves an IP address to VID table 14 with a key using a termination IP address of the frame to take out an input VID and transmits it together with the frame to a transfer processor, or if the tag is not attached, transmits the received frame to the transfer processor, as it is. An address retriever 121 of the transfer processor retrieves a VID to output port table 15 with a key using an input VID of the transmitted frame to take out an output port. ; **COPYRIGHT:** (C)2003,JPO



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